

Docket No.: INTEL-0062

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of :  
Peter HAZUCHA, Jianping XU, Gerhard SCHROM, Tanay KARNIK, Fabrice PAILLET, and Vivek K. DE :  
Serial No.: New U.S. Patent Application :  
Filed: March 31, 2004 :  
Customer No.: 34610 :  
For: RESONANCE SUPPRESSION CIRCUIT

**INFORMATION DISCLOSURE STATEMENT**

U.S. Patent and Trademark Office  
2011 South Clark Place  
Customer Window  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, Virginia 22202

Sir:

Pursuant to 37 C.F.R. 1.56, the attention of the Patent and Trademark Office is hereby directed to the reference listed on the attached PTO-1449. One copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Applicants have listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the indicated date. Applicant reserves the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered. This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith. It is further understood that the Examiner will consider information that was cited or submitted to the U.S. Patent and Trademark Office in a prior application relied on under 35 U.S.C. §120. 1138 OG 37, 38 (May 19, 1992).

- X 1. This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.
- 2. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance.
- a. I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. 1.97(e)(1).
- b. I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application

and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. 1.97(e)(2).

- c. Attached is our check no. \_\_\_\_\_ in the amount of \$240.00 in payment of the fee under 37 C.F.R. 1.17(p). Please credit or debit Deposit Account No. 16-0607 as needed to ensure consideration of the disclosed information. Two duplicate copies of this paper are attached.
- 3. This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but on or before payment of the Issue Fee. It is hereby requested that the Information Disclosure Statement be considered. Attached is our check no. \_\_\_\_\_ in the amount of \$130.00 in payment of the fee under 37 C.F.R. 1.17(i)(1). Please credit or debit Deposit Account No. 16-0607 as needed to ensure consideration of the disclosed information. Two duplicate copies of this paper are attached.
- a. I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. 1.97(e)(1).
- b. I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. 1.97(e)(2).
- X 4. To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 16-0607 and please credit any excess fees to such deposit account.

Respectfully submitted,



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MLF:DCO/kah

**Please direct all correspondence to Customer Number 34610**

<b>LIST OF PRIOR ART CITED BY APPLICANT</b>  <b>(PTO-1449)</b>				ATTY. DOCKET NO. <b>INTEL-0062</b>		APPLN. SERIAL NO. <b>New U.S. Patent Application</b>	
				APPLICANT(S) <b>Peter HAZUCHA, Jianping XU, Gerhard SCHROM, Tanay KARNIK, Fabrice PAILLET, and Vivek K. DE</b>			
				CUSTOMER NO. 34610			
				FILING DATE <b>March 31, 2004</b>		GROUP <b>To be assigned</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	*PATENT NO.	*ISSUE DATE	*INVENTOR NAME	CLASS	SUBCLASS	FILING DATE	
<b>U.S. PATENT APPLICATION PUBLICATIONS</b>							
	*PATENT APPLN. PUB. NO.	*PUB. DATE	*APPLICANT	CLASS	SUBCLASS		
<b>U.S. PATENT APPLICATIONS</b>							
	*APPLN. NO.	*FILING DATE	*INVENTOR	CLASS	SUBCLASS		
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Publisher, Place of Publication, Etc.)</b>							
	Patrik Larsson, "Resonance and Damping in CMOS Circuits with On-Chip Decoupling Capacitance;; IEEE Transactions on Circuits and Systems - I: Fundamental Theory and Applications, Vol. 45, No. 8, August 1998; pp. 849-858						
	Luca Amoroso et al., "Single Shot Transient Suppressor (SSTS) for High Current High Slew Rate Microprocessor;" 1999 IEEE; pp. 284-288						
	Michael Ang et al.; "WP 26.7 An On-Chip Voltage Regulator Using Switched Decoupling Capacitors" 2000 IEEE International Solid-State Circuits Conference; pp. 438-439						
EXAMINER				DATE CONSIDERED			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.